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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/895,319	07/02/2001	Toshiaki Shinohara	210314US2	6650
22850	7590	07/08/2003		
OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C. 1940 DUKE STREET ALEXANDRIA, VA 22314			EXAMINER NGUYEN, DILINH P	
			ART UNIT 2814	PAPER NUMBER

DATE MAILED: 07/08/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Advisory Action</b>	Application No.	Applicant(s)	
	09/895,319	SHINOHARA, TOSHIAKI	
	Examiner	Art Unit	
	DiLinh Nguyen	2814	

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**THE REPLY FILED 26 June 2003 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.** Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

**PERIOD FOR REPLY [check either a) or b)]**

a)  The period for reply expires 3 months from the mailing date of the final rejection.  
 b)  The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.  
 ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

1.  A Notice of Appeal was filed on \_\_\_\_\_. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.

2.  The proposed amendment(s) will not be entered because:

- (a)  they raise new issues that would require further consideration and/or search (see NOTE below);
- (b)  they raise the issue of new matter (see Note below);
- (c)  they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
- (d)  they present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: \_\_\_\_\_.

3.  Applicant's reply has overcome the following rejection(s): \_\_\_\_\_.

4.  Newly proposed or amended claim(s) \_\_\_\_\_ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).

5.  The a) affidavit, b) exhibit, or c) request for reconsideration has been considered but does NOT place the application in condition for allowance because: See Continuation Sheet.

6.  The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.

7.  For purposes of Appeal, the proposed amendment(s) a) will not be entered or b) will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: \_\_\_\_\_.

Claim(s) objected to: 8.

Claim(s) rejected: 1-7.

Claim(s) withdrawn from consideration: \_\_\_\_\_.

8.  The proposed drawing correction filed on \_\_\_\_\_ is a) approved or b) disapproved by the Examiner.

9.  Note the attached Information Disclosure Statement(s) ( PTO-1449 ) Paper No(s). \_\_\_\_\_.

10.  Other: \_\_\_\_\_.

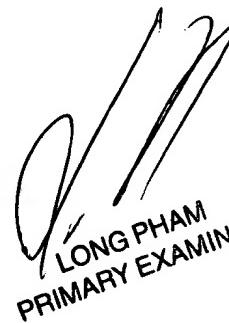
Continuation of 5. does NOT place the application in condition for allowance because: Applicant's arguments filed 6/26/03 have been fully considered but they are not persuasive.

The applicant argues that the circuit pattern layer 106 of Noda et al. does not have the function of dissipating heat.

Noda et al. disclose the circuit pattern 106 provided on the second surface of the lead frame, wherein the lead frame 103 having a first surface for mounting the semiconductor element thereon and wherein the heat generated from the semiconductor element is transferred to the circuit pattern 106 and then further transferred to a metal heat sink 104. Therefore, the circuit pattern 106 has the function of dissipating heat.

In response to applicant's argument that the references fail to show certain features of applicant's invention, it is noted that the features upon which applicant relies (i.e., the heat generated from the power element is diffused in the vertical direction with respect to the thickness of the metal block by the metal block and an area over which heat passes through an insulation layer is sufficiently larger, thereby improving the heat dissipation characteristic) are not recited in the rejected claim(s). Although the claims are interpreted in light of the specification, limitations from the specification are not read into the claims. See *In re Van Geuns*, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993).

AAPA (fig. 8) discloses the metal block 5 has the first surface and the second surface; wherein the first surface of the metal block is closer, as viewed in a vertical direction, to the lead frame than is the second surface of the metal block. Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the device of Noda et al. and Ueno et al. to increase the heat dissipation for the semiconductor package and design flexibility in mounting semiconductor device, as shown by AAPA.



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